



PRESS RELEASE
For Immediate Release

Posedge's Low Power SoC Communications Subsystem IP Delivers Superior Network Performance and Enhanced Security
New White Paper Highlights How Silicon Integration Delivers Dramatic Improvements and Maintains Interoperability

Sunnyvale, California, June 14, 2010 – Posedge Inc, a semiconductor intellectual property solutions provider, announced the availability of a new white paper which discusses the performance and security challenges associated with Internet connectivity today and the up to 10 times improvements in network performance and enhanced security that are achievable using an SoC communications subsystem IP approach.

The white paper highlights that the massive amount of software and hardware being deployed in support of the growing use of the Internet, and the multitude of vendors providing components for the OSI Layers, now makes it extremely difficult to achieve both performance and security improvements and maintain interoperability.

Posedge offers communications subsystem IP solutions, or hardware and software combinations, which provide system level functions for System on Chip (SoC) OSI Layer 2-4 support. Silicon based subsystems deliver the flexibility that ensures SoC developers can improve network performance and enhance security while maintaining interoperability. Optimal integration also significantly lowers the overall power consumption required to operate the communications connections. Posedge's communications subsystem IP has proven to be more economical when compared to the development costs for engineering similar solutions using in-house resources.

“The emergence of subsystems as semiconductor intellectual property is vital to continuing desirable economics for both semiconductor vendors and their system customers.” says Rich Wawrzyniak, Senior Analyst, Semico Research. “No where is this more evident right now than in communications. The wide range of protocols and layers that must be supported creates enormous interoperability complexities. Consolidating components is a good way to facilitate visible system level improvements while streamlining the interoperability challenges”.

Posedge partitions the communications system functions into a subsystem separate from the other system functions. This decoupling leads to architectures that are optimized for small footprints and low power. Mixing and matching protocols in software that operates on top of the hardware enables a wider range of protocol support using common hardware which promotes high reuse. Posedge also offers consulting services to help with specification development and implementation, further reducing the need for specialized communications expertise to execute a silicon design.

The white paper is available for download at www.posedge.com

About Posedge

Posedge Inc. (www.posedge.com), headquartered in Sunnyvale, CA, USA, is a leading supplier of Wired and Wireless Secure Networking Semiconductor Intellectual Property (“IP”) solutions for SoC/FPGAs. Posedge’s highly differentiated product portfolio includes Multi-Gigabit Layer2+ Switch, 10 Gbps MACsec engine, TCP Offload Engine, IPSEC Protocol Processor, Multi-Gigabit Routing Processor in addition to SoC Solutions such as SD / SDIO, Flash and Bus Protocols. Posedge’s Semiconductor “IP” and designs services have been used by semiconductor vendors including: AMD, AMCC, Broadcom, Maxim, and Texas Instruments.

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